



SH240M3RS

(240 Mega Area Sensor Module)

The SH240M3RS is a 240M CMOS area image sensor with 20k x 12k pixels. It is developed for high-performance machine vision applications. The sensor has on-chip CDS blocks to reduce Fixed Pattern Noise (FPN) and on-chip 12-bit ADC blocks to digitize the pixel output. PGA blocks before ADC blocks amplify the pixel output signals with user programmed gain values.

FEATURES

- Process Technology: 0.11mm 1P4M CMOS Image Sensor Process
- Unit Pixel: 3.5 mm X 3.5 mm
- Effective Resolution: 20204 X 11800
- Total Active Pixel Array : 20224 X 11800 pixels including right side 20 OB columns
- Progressive Scanning with rolling shutter
- Output Formats : 12b parallel Gray-coded raw data X 32 ports
- Register Control : 3-wire Serial I/F
- Max. Output Data Rate : 180MSPS per port
- Support for Block-wise Scanning
- 7-bit Programmable Gain Control in Logarithmic Scale (-6dB ~ +26dB)
- Single Power Supply Voltage: 3.3V
- Variable I/O Levels: 1.8V ~ 3.3V

Model Name	Description
SH240M3RS-AA	240M B&W Area Image Sensor with micro lens
SH240M3RS-CA	240M Mosaic RGB Area Image Sensor with micro lens